

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

UTILITY PATENT APPLICATION TRANSMITTAL LETTER

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Customer Number 2 1 8 3 9

497 U.S. РТО 0/758136

Sir:

Enclosed for filing is the utility patent application entitled:

	METHOD OF MANUFACTURING SILICON OPTOELECTRONIC DEVICE, SILICON OPTOELECTRONIC DEVICE MANUFACTURED BY THE METHOD, AND IMAGE INPUT AND/OF OUTPUT APPARATUS USING THE SILICON OPTOELECTRONIC DEVICE									
by the following named inventor(s):										
	· Eun	ı-kyung LEE, Byoι	ıng-lyong CHOI, Pil-soo AHN,	Jun-young KIM and Young-gu JIN						
X A _l	Applicant(s) suggests Figure 12 for inclusion on the front page of the patent application publication and patent.									
X Ap	Applicant(s) requests that the published application include the following assignment information: Samsung Electronics Co., Ltd., Gyeonggi-do, Republic of Korea									
· _										
. — []/ Si	mall entity sta	atus is claimed.								
Also enclosed are:										
DRAWINGS:		16 sheets of formal drawings sheets of informal drawings								
DECLARATION:		⊠ will follow	executed, is enclosed	unexecuted, is enclosed						
ASSIGNMENT:		☐ is enclosed	⊠ will follow							

Attorney Docket No.	030681-624	
Application No.	Unassigned	

CLAIM FOR PRIORITY UNDER 35 U.S.	is made in the declaration Country Appl.				Filing Date DD-MM-YYYY				
C. § 119 and/or		Korea		2003-3259					
365:		- Norou		2003-3239	17-01-2003				
									
	□ Certified columns □ Certified columns	opy(ies) enclos	ed	certified copy(ies) will follow					
OTHER PAPERS:	▼ a General Authorization for Potitions for Extensions of Times and D								
OTTICK PAPERS.	■ a General Authorization for Petitions for Extensions of Time and Payment of Fees ■ an Information Disclosure Statement								
	■ an Application Data Sheet (ADS) □								
	D			 					
The filing fee h amendment:	as been calcu	lated as follows	and in accorda	ance with the enclosed	oreliminary				
			CLAIMS						
	No. of Claims		Extra Claims	Rate	Fee				
Basic Application Fe	e (1001)				\$ 770.00				
Total Claims	35	MINUS 20 =	15	x \$18.00 (1202) =	\$ 270.00				
ndependent Claims		MINUS 3 =	00	x \$86.00 (1201) =	\$ 0.00				
f multiple dependen		esented, add \$	290.00 (1203)						
otal Application Fee		\$ 1,040.00							
Small Entity Statu	\$ 0.00								
Add Assignment Red		\$40.00 (8021)	if Assignment docu	iment is enclosed.					
OTAL APPLICATION	\$ 1,040.00								
This application is respectfully re	This application is being filed without a filing fee. Issuance of a Notice to File Missing Parts of Application is respectfully requested.								
☐ Charge	to Den	osit Account No	. 02-4800 for the fe	e due					
	to Deposit Account No. 02-4800 for the fee due. the amount of\$ 1,040.00_ is enclosed for the fee due.								
	annount of _ \$	1,040.00 IS 6	enclosed for the fee	due.					

9. 🗻 💆

Attorney Docket No. 030681-624
Application No. Unassigned

The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Please address all correspondence concerning this application to:

Burns, Doane, Swecker & Mathis, L.L.P. Customer Number **2 1 8 3 9** P.O. Box 1404 Alexandria, Virginia 22313-1404

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620

Filed: January 16, 2004

Ву

Charles F. Wieland III Registration No. 33,096

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Eun-kyung LEE et al

Group Art Unit: Unassigned

Application No.: Unassigned

Examiner: Unassigned

Filing Date:

January 16, 2004

Confirmation No.: Unassigned

Title: METHOD OF MANUFACTURING SILICON OPTOELECTRONIC DEVICE, SILICON

OPTOELECTRONIC DEVICE MANUFACTURED BY THE METHOD, AND IMAGE INPUT AND/OR

OUTPUT APPARATUS USING THE SILICON OPTOELECTRONIC DEVICE

GENERAL AUTHORIZATION FOR PETITIONS FOR EXTENSIONS OF TIME AND PAYMENT OF FEES

Assistant Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §1.136(a)(3), the U.S. Patent and Trademark Office is hereby provided with a general authorization to treat any concurrent or future reply requiring a petition for an extension of time for its timely submission as containing a request therefor for the appropriate length of time.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. § 1.17 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, البال.P.

P.O. Box 1404 Alexandria, Virginia 22313-1404 (703) 836-6620

Date: January 16, 2004

Charles F. Wieland III Registration No. 33,096

